


Title: <i>Line Receiver</i>			 NTB INTERSTAATLICHE HOCHSCHULE FÜR TECHNIK BUCHS <i>NTB Interstate University of Applied</i> <i>Sciences of Technology Buchs</i> <i>Werdenbergstrasse 4</i> <i>9471 Buchs Switzerland</i>
Description: <i>Description</i>			
Name: <i>Alison Jäggi</i>		Checked: <i>Aaron Leander</i>	
Size: <i>A4</i>	Number: <i>*</i>	Revision: <i>1.2</i>	
Date: <i>22.07.2014</i>	Time: <i>09:36:17</i>	Sheet <i>1</i> of <i>1</i>	
File: <i>D:\SYN\ExpSystem\Schnittstellen\LineReceiver\Elektronik\Line_Receiver.SchDoc</i>			

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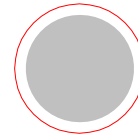
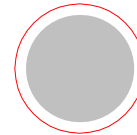
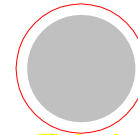
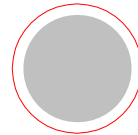
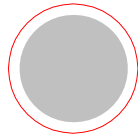
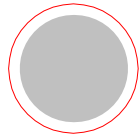
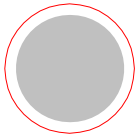
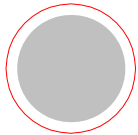
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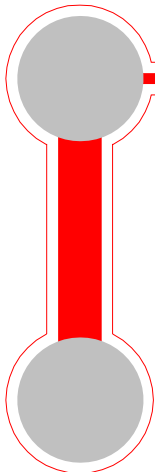
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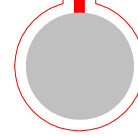
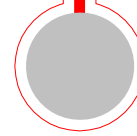
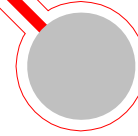
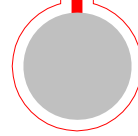
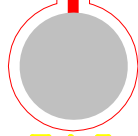
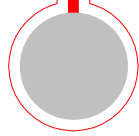
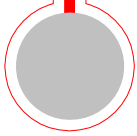
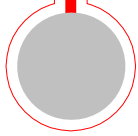
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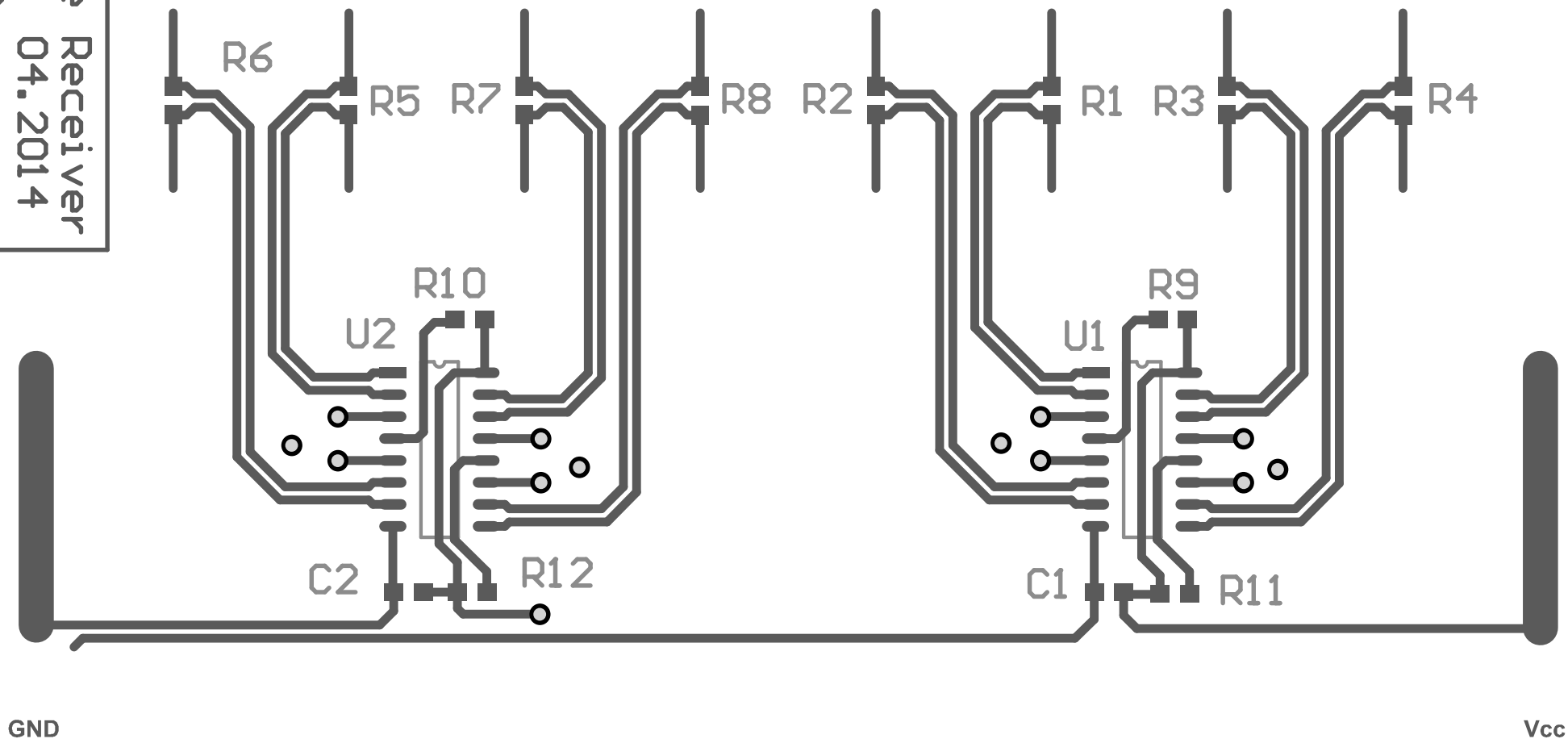
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GND

Line Receiver
1-2
04.2014
JAGA



Comment	Description	Designator	Footprint	LibRef	Quantity
Exp_Board			EXP_BOARD	Exp_Board	1
100nF	Capasistor SMD	C1, C2	0603	C	2
Hole_2mm		P1, P2, P3, P4, P5, P6, P7, P8, P9, P10, P11, P12, P13, P14, P15, P16, P17, P18, P19, P20, P21, P22, P23, P24	Hole_2mm	Hole_2mm	24
120R	Resistor SMD	R1, R2, R3, R4, R5, R6, R7, R8	0603	R	8
0R	Resistor SMD	R9, R10, R11, R12	0603	R	4
AM26LV32CD	AM26LV32CD	U1, U2	SOP16	AM26LV32CD	2